KP270-PO ER TH RISTOR

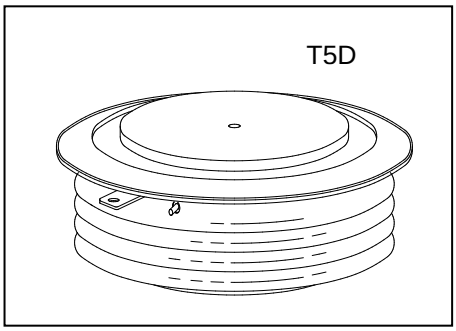
78000-8500 DRM

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FREE FLOATING T PE TH RISTOR FOR PHASE CONTROL APPLICATIONS

F :

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. B **8500**
. D



ELECTRICAL CHARACTERISTICS AND RATINGS

B - O S

Device Type	V_{RRM} (1)	V_{DRM} (1)	V_{RSM} (1)
KP270/78	6900	6900	7800
KP270/80	7100	7100	8000
KP270/82	7300	7300	8200
KP270/85	7600	7600	8500

V_{RRM} = Repetitive peak reverse voltage
V_{DRM} = Repetitive peak off state voltage
V_{RSM} = Non repetitive peak reverse voltage (2)

Notes:
(1) All voltage ratings are specified for an applied 50Hz sinusoidal waveform over the temperature range -40 °C to +110 °C.
(2) 10 msec. max. pulse width
(3) Maximum value for Tj = 110 °C.
(4) Minimum value for linear and exponential waveshape to 67% rated V_{DRM}. Gate open. Tj = 110 °C.
(5).The value of di/dt is established in accordance with EIA/NIMA Standard JB/T 8950.2-2013

Repetitive peak reverse leakage and off state leakage	I_{RRM}/I_{DRM}	2 mA 100 mA (3)
Critical rate of voltage rise	dv/dt (4)	2000 V/µ

C - O S

P	S	M .	M .	T .	U	C
Average value of on-state current	$I_{T(AV)}$		270		A	Sinewave,180° conduction,T_c=70°C
RMS value of on-state current	I_{TRMS}		424		A	Nominal value
Peak one cycle surge (non repetitive) current	I_{TSM}		4000		A	10.0 msec (50Hz), sinusoidal wave-shape, 180° conduction, T_j = 110 °C
I square t	I^2t		$8.0x10^4$		A^2s	10 msec
Latching current	I_L		1000		mA	V_D = 12 V; R_L= 12 ohms
Holding current	I_H		200		mA	V_D = 12 V; I = 2.5 A
Peak on-state voltage	V_{TM}		2.8		V	I_{TM}=500A;Tj =25°C
Threshold voltage, low-level	V_{TO}		1.25		V	Tj=110°C
Slope resistance, low-level	r_T		3.0		mΩ	500A to 1300A
Critical rate of rise of on-state current(5)	di/dt		150		A/µs	Tj=110°C; V_D = 0.67 V_{DRM}; f=50Hz; I_{TM}=1000A

G

P	S	M .	M .	T .	U	C
Peak gate power dissipation	P_{GM}		20		W	
Average gate power dissipation	$P_{G(AV)}$		4		W	
Gate-trigger current	I_{GT}		200		mA	V_D =12 V;R_L = 3 ohms;T_j = +25 °C
Gate- trigger voltage	V_{GT}	0.7	2.6		V	V_D = 12 V;R_L = 3 ohms;T_j = +25 °C
Peak negative voltage	V_{GRM}		10		V	

D

P	S	M .	M .	T .	U	C
Delay time	t_d			3.0	μs	I_{TM}=100A; V_D=67%V_{DRM} V_G=30V; R_G=10ohms; t_r=0.1μs; t_p=20μs
Turn-off time (with V_R = -5 V)	t_q			800	μs	I_{TM} =1000A; di/dt =-10A/μs; V_R =100V; dv/dt=30V/μs ; V_D= 67%V_{DRM} ;Tj=110
Reverse recovery charge	Q_{rr}		2500		μC	I_{TM}=1000A di/dt=-10A/μs; V_R=100V; T_j=110

THERMAL AND MECHANICAL CHARACTERISTICS AND RATINGS

P	S	M .	M .	T .	U	C
Operating temperature	T_j	-40	+110		°C	
Storage temperature	T_{stg}	-40	+140		°C	
Thermal resistance - junction to case	$R_{\Theta(j-c)}$		0.045		°C/W	Double sided cooled
Thermal resistance - case to heatsink	$R_{\Theta(c-s)}$		0.008		°C/W	Double sided cooled
Mounting force	F	14	16	15	kN	
Weight						

